

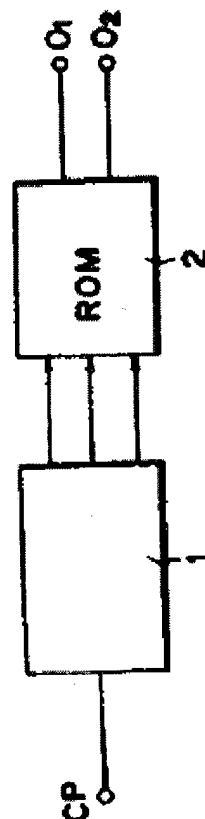
DIGITAL PATTERN GENERATING CIRCUIT

Patent number: JP57185720
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Applicant: AKAI DENKI KK
Classification:
- international: H03K3/78
- european:
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Abstract of JP57185720

PURPOSE: To simplify the pattern change, by using a counter output as an address for read only memory (ROM) and storing a desired digital pattern in the ROM.

CONSTITUTION: A counter 1 counts a clock pulse CP and an output of each digit of the counter is given to an ROM2 as an address. A storage content to output a desired digital pattern is stored in the ROM2 and a desired digital pattern is obtained at terminals 01 and 02.



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